

| | Type | L # | Hits | Search Text | DBs | Time Stamp |
|----|------|-----|-------|---|-------|---------------------|
| 1 | IS&R | L1 | 480 | ((455/131) or (455/132) or (455/144) or (455/127.3) or (455/127.4)).CCLS. | USPAT | 2004/06/14 08:08 |
| 2 | BRS | L2 | 44281 | 455/\$.ccls. | USPAT | 2004/06/14 08:09 |
| 3 | BRS | L3 | 31087 | 375/\$.ccls. | USPAT | 2004/06/14 08:09 |
| 4 | BRS | L4 | 44281 | 1 or 2 | USPAT | 2004/06/14 08:09 |
| 5 | BRS | L5 | 70592 | 3 or 4 | USPAT | 2004/06/14 08:09 |
| 6 | BRS | L6 | 41593 | 5 and (signal processing) and (semiconductor integrated circuit) | USPAT | 2004/06/14 08:11 |
| 7 | BRS | L8 | 17787 | 6 and (firt amplifier) and (second amplifier) and (third amplifier) | USPAT | 2004/06/14 08:13 |
| 8 | BRS | L11 | 17549 | 8 AND (RECEPTION CIRCUIT) AND (FIRT AMPLIFIER) AND (SECOND AMPLIFIER) | USPAT | 2004/06/14 08:15 |
| 9 | BRS | L14 | 9717 | 11 AND (DIRECT CONVERSION) AND AMPLIFIER | USPAT | 2004/06/14 08:17 |
| 10 | BRS | L15 | 2165 | 14 AND (AMPLIFIER ACTIVATE\$3) AND (AMPLIFIER DEACTIVATE\$3) | USPAT | 2004/06/14 08:19 |
| 11 | BRS | L16 | 1 | 15 AND (FREQUENCY ADJ CONVERSION) AND AMPLIFIER | USPAT | 2004/06/14 08:23 |
| 12 | BRS | L17 | 8 | 15 AND AMPLIFIER AND IMPEDANCE | USPAT | 2004/06/14 08:24 |
| 13 | BRS | L18 | 2165 | 15 AND AMPLIFIER | USPAT | 2004/06/14 08:24 |
| 14 | BRS | L20 | 120 | 18 AND ACTIVATE\$3 AND DEACTIVATE\$3 AND AMPLIFY\$3 | USPAT | 2004/06/14 08:26 |
| 15 | BRS | L21 | 84 | 20 AND (PLURALITY OF AMPLIFIERS) AND AMPLIFIERS | USPAT | 2004/06/14 08:55 |